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R Gordon, S McNealy, T Ng, Y Rotblum, M Tate - Custom Integrated Circuits Conference, 1989., Proceedings of ..., 1989 - ieeexplore.ieee.org

An N-Bus Datapath Compiler for IC Design r ... are run using Lsim to verify the design's logic ... For regular structures, the datapath compiler can create layouts ...

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MacPitts: An approach to silicon compilation

JR Southard - Computer, 1983 - ieeexplore.ieee.org

... There are at least two separable aspects to IC design. One is the rigorous specification of the integrated circuit's function. ...

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System-level verification-a comparison of approaches

R Turner, QD Syst, CA San Jose - IEEE International Workshop on Rapid System Prototyping, ..., 1999 - ieeexplore.ieee.org

... software into a ROM code file, 2) Compile hardware design ... doing ad hoc testing of the whole IC at once ... can do focussed testing of specific parts of the design. ...

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Design of heterogeneous ICs for mobile and personal communication systems- ► ucsd.edu

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G Goossens, I Bolsens, B Lin, F Catthoor - IEEE/ACM International Conference on Computer-Aided Design, ..., 1994 - ieeexplore.ieee.org

... Figure 5: Outline of an IC design environment for ... more specific requirements for the different design tools ... level synthesis systems and software compilers for ...

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Creating a mixed-signal simulation capability for concurrent ICdesign and test program development

T Austin, T Inc, CA Sunnyvale - Test Conference, 1993. Proceedings., International, 1993 - ieeexplore.ieee.org

... on a device, 1) critical element affecting IC performance could ... tool which test engineer uses to compile the test ... a means of combining the design simulator's ...

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Compatibility verification for web service choreography- ► psu.edu [PDF]

H Foster, S Uchitel, J Magee, J Kramer, IC London - IEEE International Conference on Web Services, 2004. ..., 2004 - ieeexplore.ieee.org

... BZ, UK {hf1, su2, jnm, jk}@doc.ic.ac.uk ... is easier in early phases of design, collaboration will ... into a model representation (FSP), 5) compile processes into ...

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IC Park, S Kang, Y Yi, D KAIST, S Korea - Computer Aided Design, 2003. ICCAD-2003. International ..., 2003 -

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... and Engineering Foundation through Lhc MICROS center and by IC Design Education Center ... instruction decoding from the simulation run-time to the **compile-time** [6 ...]

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An intelligent analog IC design system based on manipulation ofdesign equations

K Swings, G Giesen, W Sansen - Custom Integrated Circuits Conference, 1990., Proceedings of ..., 1990 -

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... 1. Introduction Designing full-custom IC's has generated ... are therefore the following :

t **Compile** analog functions ... forecast every unfeasible **design**, a redesign ...

Web Search

[PS] ► Logic Synthesis

S Design, V Netlist, RTLC Prepare, C Design - ics.ele.tue.nl

... RAMs, registers, Page 3. IC design and logic synthesis 3 basic logic cells, and regularly structured data path components. Its result ...

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[PDF] ► Synthesis using Synopsys Design Compiler Tutorial The Synthesis Flow (What, How & Why?)

M Ibrahim Al-Behwashi, A El-Maleh, F Semester - citeseerx.ist.psu.edu

... Synthesis : is the process that generates a gate-level netlist for an IC design that has ... Performing a High-Effort **Compile**: - Pushes **Design** Compiler to ...

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Key authors: **J Southard - H Foster - B Murray - R Jain - S Uchitel**

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